

03-23-01

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09/814255  
03/21/01

March 21, 2001

Attorney Docket No.: 07977-107002

**Box Patent Application**  
Commissioner for Patents  
Washington, DC 20231

Presented for filing is a new divisional patent application of:

**Applicant:** SHUNPEI YAMAZAKI, HIDEOMI SUZAWA AND SATOSHI  
TERAMOTO

**Title:** SEMICONDUCTOR DEVICE AND ITS MANUFACTURING  
METHOD

Enclosed are the following papers, including those required to receive a filing date  
under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	33
Claims	5
Abstract	1
Declaration	2
Drawing(s)	14

**Enclosures:**

- Rule 63 declaration: Copy from a previous application under rule 63(d) for continuation or divisional only.
- Information Disclosure Statement: Applicants call attention to documents listed on the attached forms PTO-1449 (4 pages) and PTO-892 (2 pages) from the parent case. Per Rule 97(d), copies of those documents are not provided.
- Postcard.

This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 08/769,371, filed December 19, 1996. The disclosure of the prior application is considered part of (and is incorporated by reference in) the disclosure of this application.

## CERTIFICATE OF MAILING BY EXPRESS MAIL

Express Mail Label No. EL584781744US

I hereby certify under 37 CFR §1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D C 20231.

Date of Deposit March 21, 2001

Signature

Gildardo Vargas

Typed or Printed Name of Person Signing Certificate

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Federick P. Fish  
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Under 35 USC §119, this application claims the benefit of foreign priority applications filed in Japan, Serial Number 7-349667, filed December 20, 1995, and Serial Number 7-351445, filed December 25, 1995, and Serial Number 8-023073 filed January 16, 1996.

Preliminary Amendment:

Page 1 of the specification, before line 1, insert -- This is a divisional of U.S. application serial no. 08/769,371, filed December 19, 1996 (pending). --

The prior application is assigned of record to Semiconductor Energy Laboratory, Co. Ltd., a Kanagawa, Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on December 19, 1996 at 8374/0327.

Basic filing fee	\$710
Total claims in excess of 20 times \$18	\$126
Independent claims in excess of 3 times \$80	\$320
Total filing fee:	\$1156

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

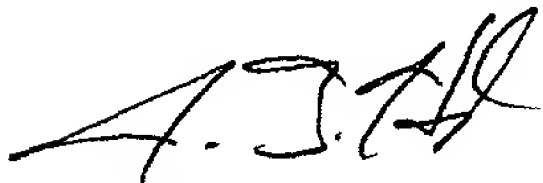
If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (858) 678-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please send all correspondence to:

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Respectfully submitted,



James T. Hagler  
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Enclosures  
JTH/krc  
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